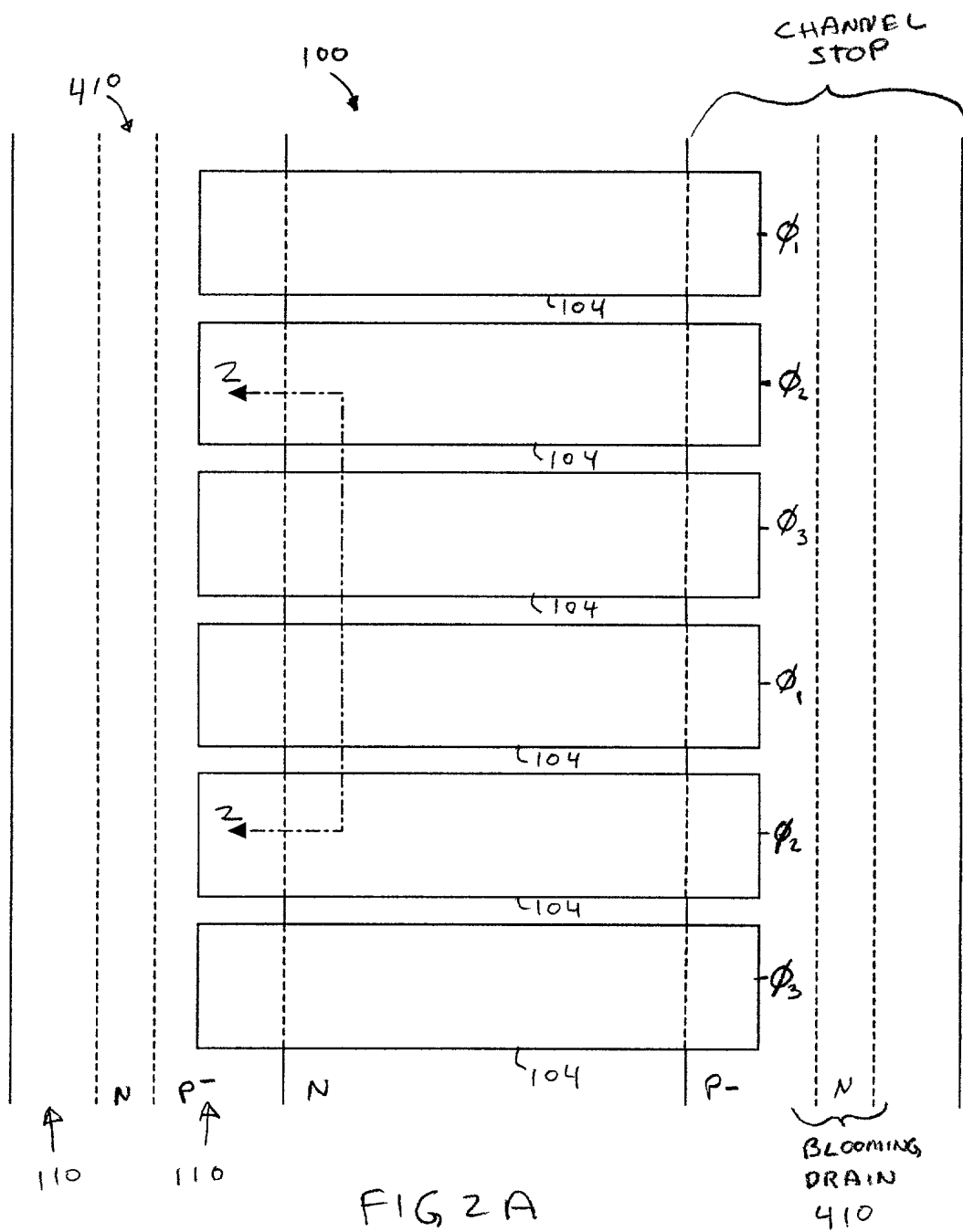


A block diagram showing the system architecture. It includes four main components: 'OPTICS' (labeled 50), 'DIGITAL SIGNAL PROC' (labeled 60), 'POWER SUPPLY' (labeled 70), and a central vertical block (labeled 10). An arrow points from the 'OPTICS' block to the central block. Another arrow points from the central block to the 'DIGITAL SIGNAL PROC' block. A third arrow points from the 'POWER SUPPLY' block to the central block. A fourth arrow points from the 'POWER SUPPLY' block to the 'DIGITAL SIGNAL PROC' block.

FIG 1A



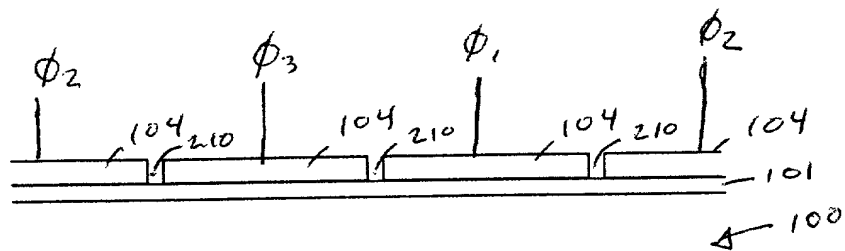


FIG 2B

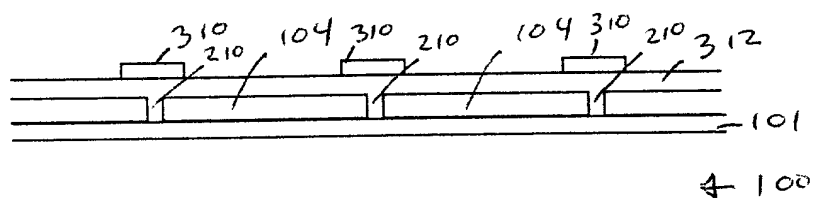


FIG 7

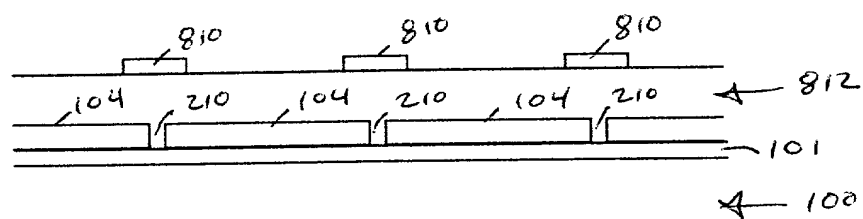
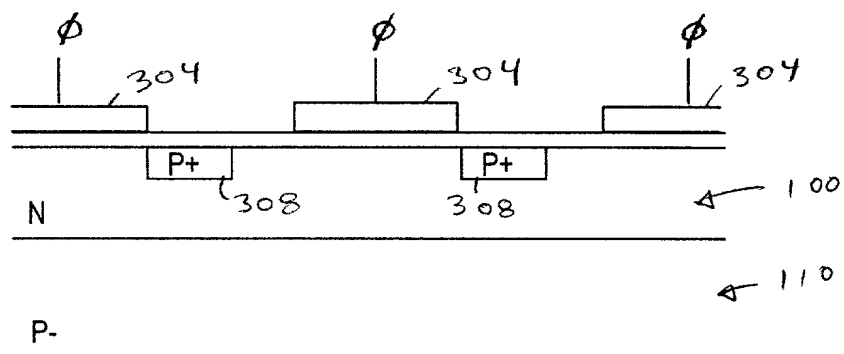
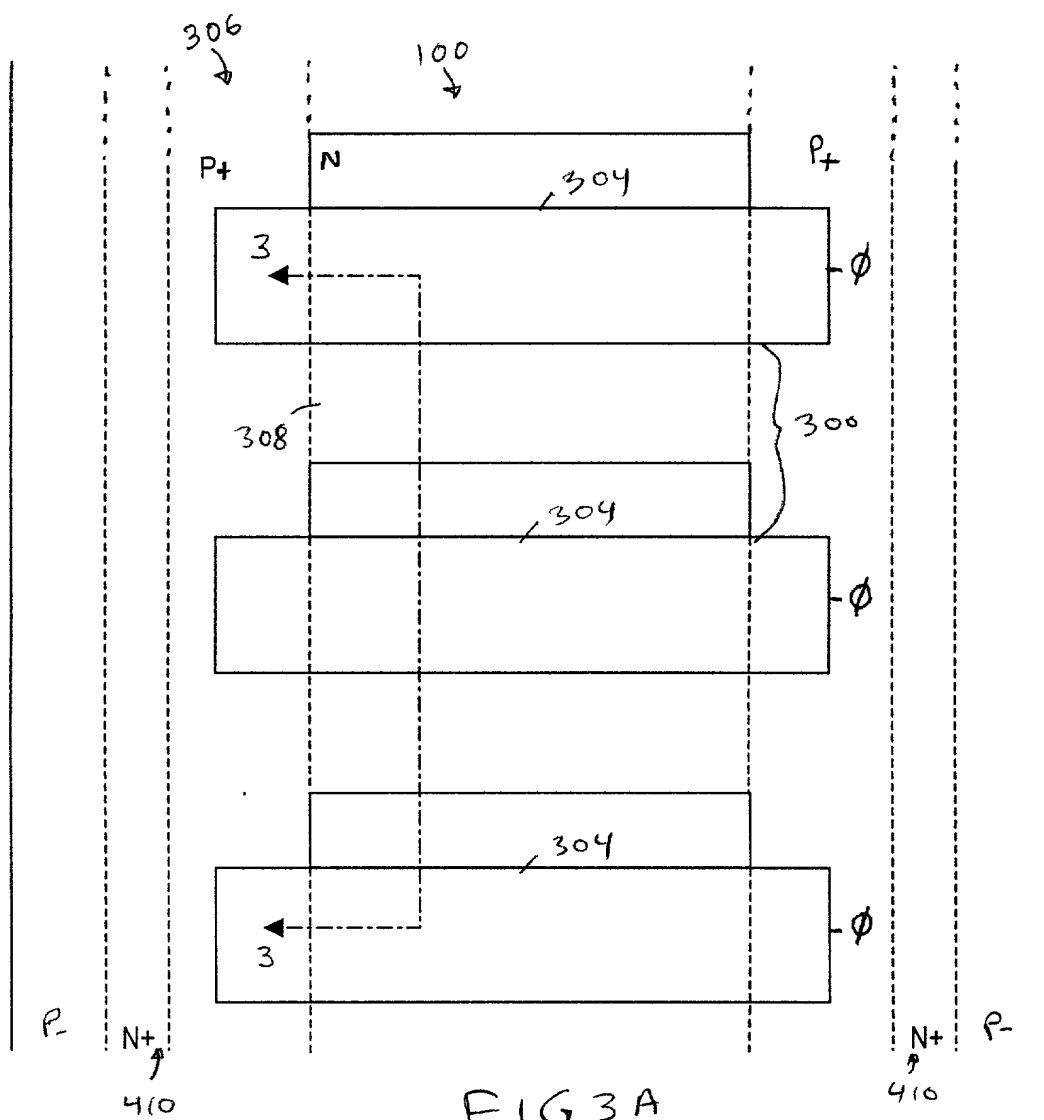


FIG 8



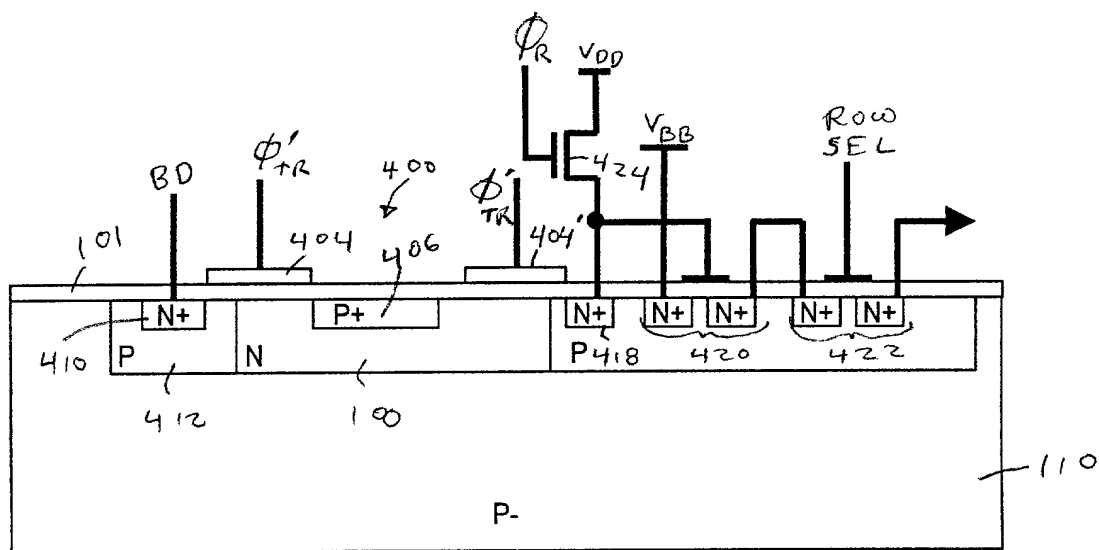
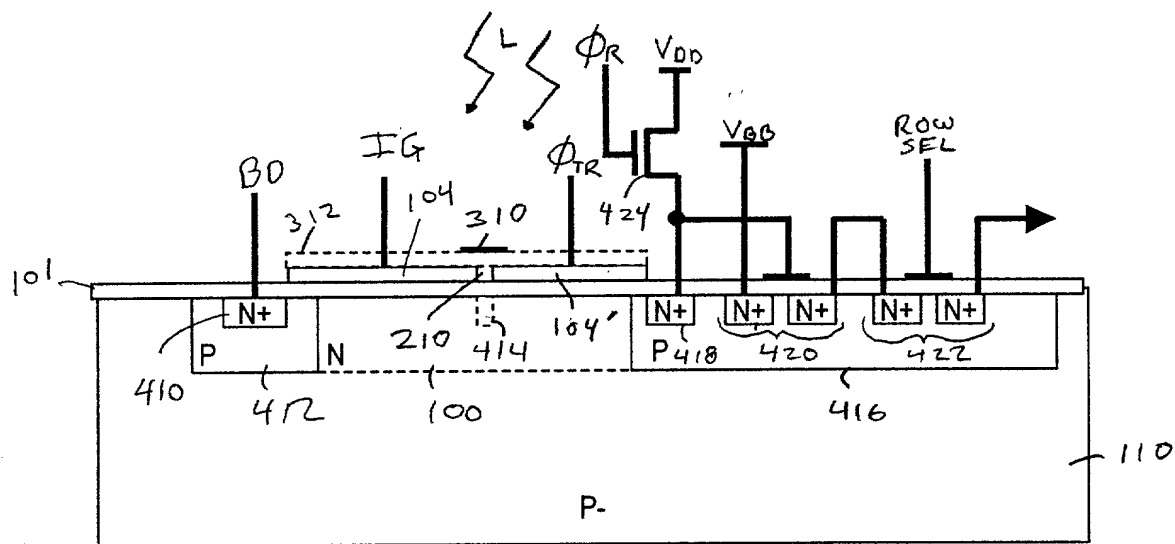


FIG 4D

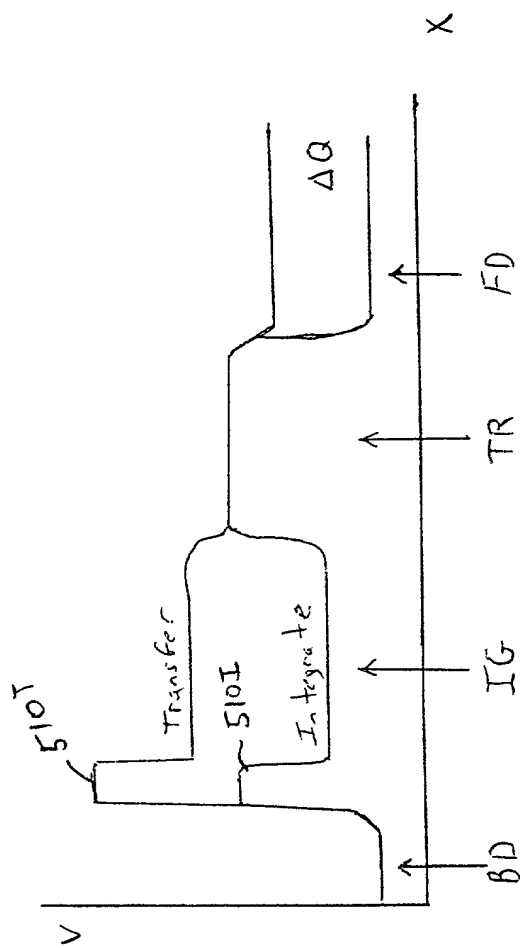


FIG. 5

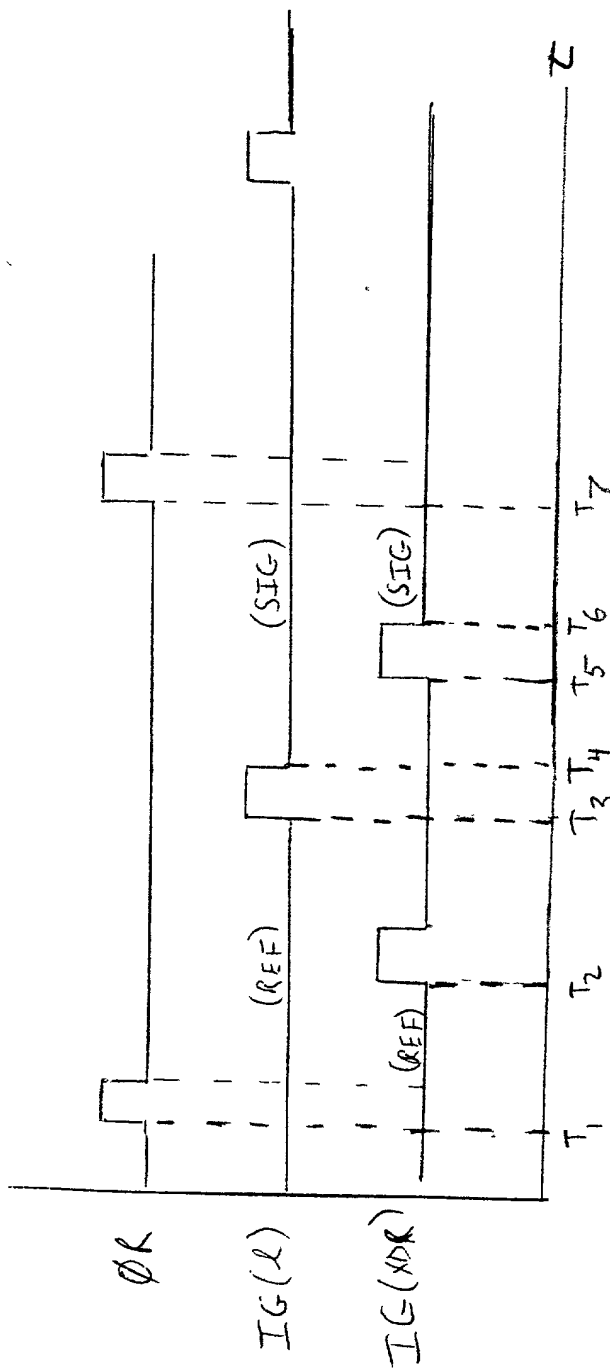


FIG. 6